10869 U.S. PTO 09/853005	Subclass		ð١		PATENT NUMBER
		Class	ISSUE CLAS		
				U.S. UTILITY Patent Application	

SCANNED WILLTY Patent Application

O.I.P.E. PATENT DATE

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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
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Chau-Cha Chi-Che Chen-Pin	Tsai		-	2187	of Baken
Peripher w method E	al device	interfa	ce chip c	ache and da	ta synchronization
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ISSUING CLASSIFICATION									
ORIGINA	\L	. CROSS REFERENCE(S)							
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION								·	
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TERMINAL		DRAWINGS		CLAIMS ALLOWED		
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
☐ The term of this patent				NOTICE OF ALI	OWANCE MAILED	
subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)		, ==-	
The term of this patent shall not extend beyond the expiration date	\$					
of U.S Patent. No.				ISSUE FEE		
				Amount Due	Date Paid	
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this patent have been disclaimed.	(Legal Instrume	ents Examiner)	(Date)			
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